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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Manpreet S. Khaira et al.
Title: LOGIC VERIFICATION IN LARGE SYSTEMS
Attorney Docket No.: 884.107US1

PATENT APPLICATION TRANSMITTAL

BOX PATENT APPLICATION

Assistant Commissioner for Patents
Washington, D.C. 20231

We are transmitting herewith the following attached items and information (as indicated with an "X"):

- Utility Patent Application under 37 CFR § 1.53(b) comprising:
 Specification (15 pgs, including claims numbered 1 through 28 and a 1 page Abstract).
 Formal Drawing(s) (4 sheets).
 Signed Combined Declaration and Power of Attorney (4 pgs).
 Check in the amount of \$1,372.00 to pay the filing fee.
 Assignment of the invention to Intel Corporation (2 pgs) and Recordation Form Cover Sheet.
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| TOTAL CLAIMS | 28 - 20 = | 8 | x 18 = | \$144.00 |
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| [] MULTIPLE DEPENDENT CLAIMS PRESENTED | | | | \$0.00 |
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